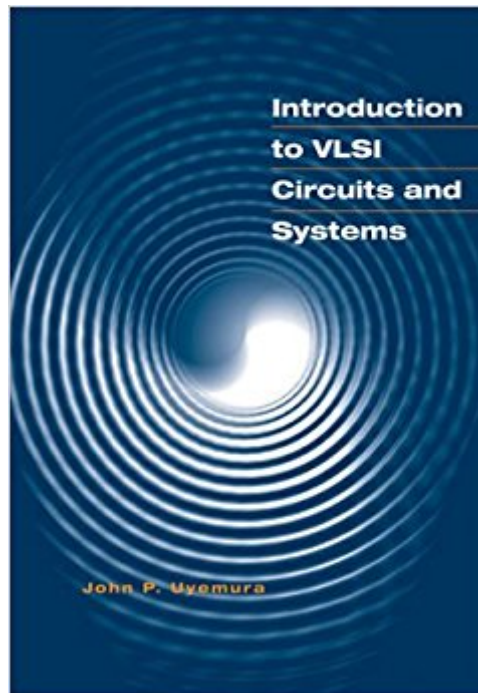


The book was found

# Introduction To VLSI Circuits And Systems



## Synopsis

Presents modern CMOS logic circuits, fabrication, and layout in a cohesive manner that links the material together with the system-level considerations. \* Chapter on Verilog HDL allows for rapid start-up. \* Illustrates the top-down design procedure used in modern VLSI chip design with an emphasis on variations in the HDL, logic, circuits and layout.

## Book Information

Hardcover: 656 pages

Publisher: Wiley; 1 edition (July 30, 2001)

Language: English

ISBN-10: 0471127043

ISBN-13: 978-0471127048

Product Dimensions: 6.9 x 1.2 x 10 inches

Shipping Weight: 2.7 pounds (View shipping rates and policies)

Average Customer Review: 5.0 out of 5 stars [See all reviews](#) (7 customer reviews)

Best Sellers Rank: #47,125 in Books (See Top 100 in Books) #3 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI](#) #13 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Design](#) #77 in [Books > Engineering & Transportation > Engineering > Telecommunications & Sensors](#)

## Customer Reviews

If you want to jump into VLSI and get VERY far in one semester, this book is great. You will need Cadence and I also recommend as a challenge to yourself constructing your own 8-bit, 8 word ALU with 3 control bits and 8 operations  $A+BA-BA--A++A$  nand  $BA$  xor  $BA'A$  nor  $Bl$  it will be a good learning experience, specially if you try to optimize it and add a barrel shifter with a 2 clock operation cycle.

I am an EE PhD student in my first year and have to design a mixed analog/digital chip. This book is one of the best technical books I have ever read. Well structured, thorough but does not give you so much detail that you risk drowning. This book actually is fun to read.

Currently using this book for a class in VLSI design. Book is well written and easy to read. Stress fundamentals and design concepts. Highly recommended!

This book is very well written and easy to understand. The author did an excellent job organizing the topics and explaining the key points thoroughly.

[Download to continue reading...](#)

Circuits, Interconnections, and Packaging for Vlsi (Addison-Wesley VLSI systems series)  
Introduction to VLSI Circuits and Systems Advances in 3D Integrated Circuits and Systems (Series on Emerging Technologies in Circuits and Systems) Design of 3D Integrated Circuits and Systems (Devices, Circuits, and Systems) Low-Voltage/Low-Power Integrated Circuits and Systems: Low-Voltage Mixed-Signal Circuits (IEEE Press Series on Microelectronic Systems) CMOS VLSI Design: A Circuits and Systems Perspective (3rd Edition) CMOS VLSI Design: A Circuits and Systems Perspective Principles of Transistor Circuits, Eighth Edition: Introduction and guide to the design of amplifiers, function generators, receivers and digital circuits Introduction to Embedded Systems: Using ANSI C and the Arduino Development Environment (Synthesis Lectures on Digital Circuits and Systems) VLSI Design Techniques for Analog and Digital Circuits (McGraw-Hill Series in Electrical Engineering) PSPICE and MATLAB for Electronics: An Integrated Approach, Second Edition (VLSI Circuits) CMOS Nanoelectronics: Analog and RF VLSI Circuits VLSI Analog Signal Processing Circuits: Algorithm, Architecture, Modeling, and Circuit Implementation Delay Fault Testing for VLSI Circuits (Frontiers in Electronic Testing) Introduction to VLSI Systems: A Logic, Circuit, and System Perspective Electronic Circuits: The Definitive Guide to Circuit Boards, Testing Circuits and Electricity Principles VLSI Digital Signal Processing Systems: Design and Implementation VLSI Test Principles and Architectures: Design for Testability (The Morgan Kaufmann Series in Systems on Silicon) Analog Design for CMOS VLSI Systems (The Springer International Series in Engineering and Computer Science) VLSI Chip Design with the Hardware Description Language VERILOG: An Introduction Based on a Large RISC Processor Design

[Dmca](#)